

PATENT NUMBER

U.S. **UTILITY** Patent Application

TO	O.I.P.E.	PATENT DATE
SCANNED AC4	Q.A. AG.	

APPLICATION NO. 09/813240	CONT/PRIOR F	CLASS 257	SUBCLASS	ART UNIT 2811	EXAMINER <i>[Signature]</i>
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APPLICANTS

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ETUDE

Semiconductor wafer made from silicon and method for producing the semiconductor wafer

PTO-204
1289

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div>		CLAIMS ALLOWED	
			<div>Total Claims</div>	<div>Print Claim for O.G.</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <div>_____</div> <div>_____</div>	<div>_____ (Assistant Examiner) _____ (Date)</div> <div>_____ (Primary Examiner) _____ (Date)</div>		NOTICE OF ALLOWANCE MAILED	
			ISSUE FEE <div>Amount Due</div> <div>Date Paid</div>	
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